

ANALOG/DIGITAL MIXED INTEGRATED CIRCUIT

Abstract

An IC chip 10 is divided into the analog circuit area 1 and a digital circuit area 2 in its layout. A clock generator circuit 6 that generates a clock signal CK is arranged within the digital circuit area 2, and a switching circuit 4 that performs switching operations by the clock signal CK is also arranged within the digital circuit area 2. This enables shortening of the wiring length of the clock line 9, which is routed from the clock generator circuit 6 to the switching circuit 4, and also enables the distance between the clock line 9 and the analog circuits within the analog circuit area 1 to be as great as possible. Through this, inconvenience where digital noise caused by the clock signal flowing through the clock line 9 jumps into analog circuits can be suppressed.